

| Pin No. | In/Out | Port Name | Function |
|---------|--------|---------------|---|
| 63 | O | Not used | This terminal keeps Low signal. |
| 64 | O | Not used | This terminal keeps Low signal. |
| 65 | O | T2.DATA.OUT | EDIT 5pin data output factory. |
| 66 | I | T2.DATA.IN | EDIT 6pin data clock output for factory. |
| 67 | O | T2.CLOCK | EDIT 7pin data input for factory. |
| 68 | O | T-BUS/IC.OUT | The Serial data output for OSD microprocessor and FIP driver. |
| 69 | I | T-BUS/IC.IN | The Serial data input for OSD microprocessor and FIP driver. |
| 70 | O | T-BUS/IC.CLK | The Serial clock output for OSD microprocessor and FIP driver. |
| 71 | O | IIC.CLOCK | Tuner and HI-FI audio IC serial clock output. |
| 72 | I/O | IIC.DATA | Tuner and HI-FI audio IC serial data input/output. |
| 73 | O | 125Hz | Oscillator output for main clock adjustment. |
| 74 | O | Not used | This terminal keeps Low signal. |
| 75 | I | POWER FAIL(L) | Power failure detection. |
| 76 | O | CAP.R/F | Capstan rotation direction control. Reverse : High Forward : Low |
| 77 | O | CAP.ET | Capstan torque control. |
| 78 | O | CYL.ET | Cylinder torque control. |
| 79 | I | S.REEL.PULSE | Supply reel pulse input. |
| 80 | I | T.REEL.PULSE | Take-up reel pulse input. |
| 81 | I | Not used | Not used |
| 82 | O | Not used | Not used |
| 83 | I | SHORT DN | DC voltage (BIAS(H)) detection. When detecting High at BIAS (H) during BIAS (H) is set to except high, complsory power is turned off (Self test indication display F08). When detecting Low at BIAS (H) during BIAS (H) is set to high, complsory power is turned off (Self test indication display F07). |
| 84 | I | C. SYNC | Composite sync signal input. |
| 85 | I | CAP. FG | Capstan FG input. |
| 86 | I | CYL. PFG | Cylinder PFG input. |
| 87 | I | SECAM24(H) | Video head selection input. In SECAM model, this port is high when using 24u head chip for LP mode is mounted on the cylinder. |
| 88 | - | GND | GND |
| 89 | O | OREF | Op amp output for 1/2 VDD reference. |
| 90 | I | IREF | Op amp input for 1/2 VDD reference. |
| 91 | I | CTL. HEAD(-) | Control signal (-) input. |
| 92 | I | CTL. HEAD(+) | Control signal (+) input. |
| 93 | O | CTL. AMP. SW | Control amp switching output. |
| 94 | I | CTL. AMP. IN | Control amp input. |
| 95 | I | CTL. AMP. REF | Control amp reference input. |
| 96 | - | CTL. AMP. GND | Control amp GND. |
| 97 | O | PB. CTL. OUT | Playback Control signal output. |
| 98 | - | 5V(A) | Analog 5V. |
| 99 | - | 5V(AD) | 5V. |
| 100 | O | Not used | This terminal Low signal. |